VILNIUS GEDIMINAS TECHNICAL UNIVERSITY

## FACULTY OF ELECTRONICS

Department of Computer Science and Communications Technologies

#### Name Surname

### THESIS TITLE (in English)

#### Bachelor Thesis

Computer Engineering Study Programme, State Code 6121EX044

Embedded Computers and their Programming Specialization

Electronic and Electrical Engineering Study Area

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| --- | --- | --- | --- | --- |
|  | **Supervisor** |  | Prof Dr. N. Surname |  |
|  |  |  | (*scientific title and degree, name, surname*) |  |
| (delete this row if no Consultant) | **Consultant** |  | Assist N. Surname |  |
|  |  |  | (*scientific title and degree, name, surname*) |  |